Optimizing GPU Usage for Deep Learning Workloads: Insights and Strategies for Enhanced Efficiency

13th June 2023

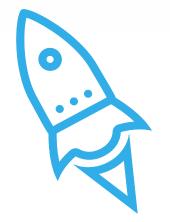
Pierrick Pochelu – PCOG







- 1. Importance of GPU workload (data from SLURM & Survey)
- 2. GPU workload challenges & potential methods
- 3. Accelerator trends and first feedbacks with IPU
- 4. User learning activity (User perspective)
- 5. Conclusion & My actions



1. Importance of GPU workload







GPU more and more useful in HPC centers for dense computing

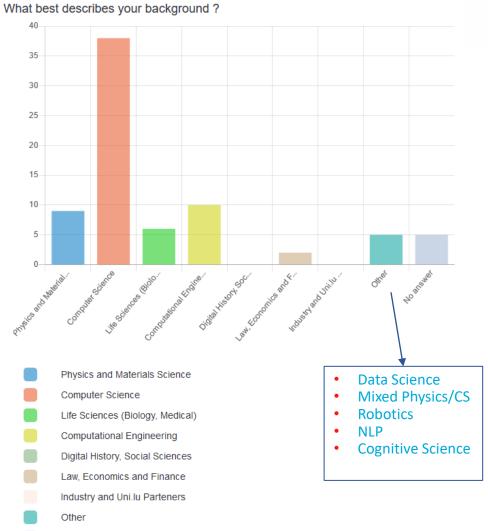
Statistics from UL HPC SLURM:

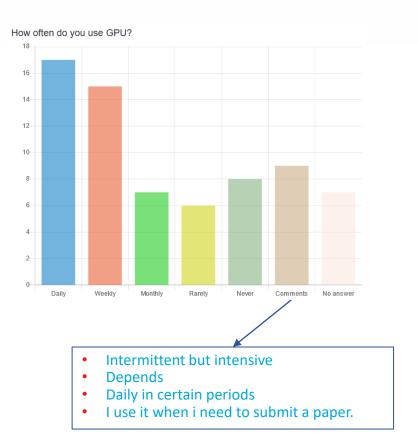
	Iris GPU	Aion CPU	
From 1-1-2023 to 12-6- 2023	64.3%	57%	
Last 2 weeks	Iris GPU Utilisation over last 2 weeks100%100%0.000 <td cols<="" td=""><td>Alon CPU Utilisation over last 2 weeks</td></td>	<td>Alon CPU Utilisation over last 2 weeks</td>	Alon CPU Utilisation over last 2 weeks

Source: Data from ULHPC Sys Admin

GPU utilization Survey

No answer





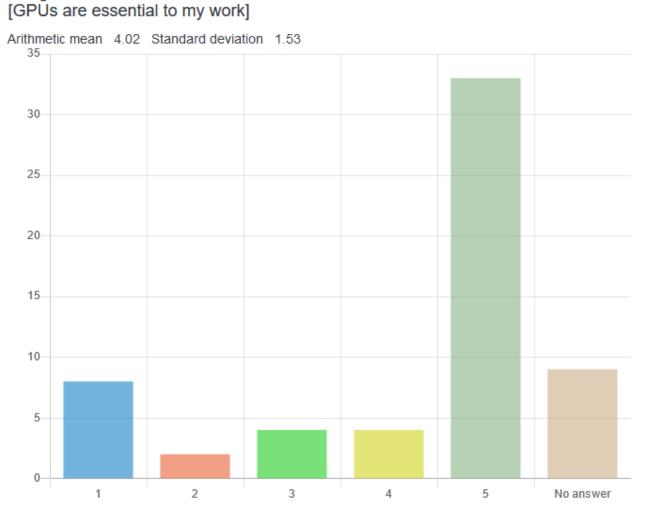
Survey submitted to UL HPC users, 75 participants, no personally indentifiable information was disclosed, conducted withulsurvey.uni.lu







At which extend those claim are true ? 5 means "strongly agree" and 1 "strongly disagree".



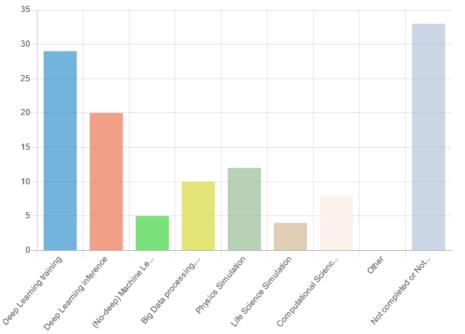
2. GPU workload challenges & potential methods



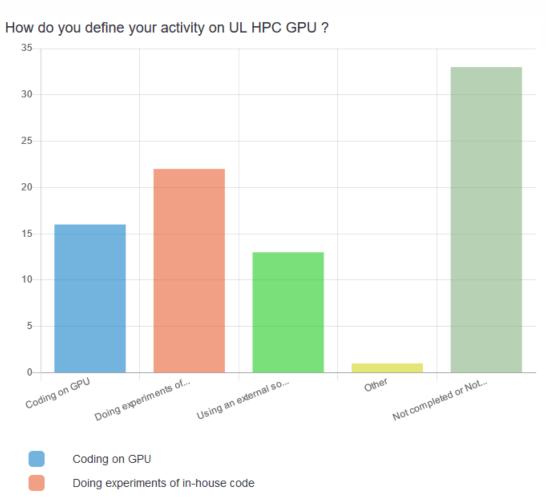
GPU workload ?



What type of tasks do you primarily perform that require significant computational resources? Please select the appropriate option:



- Deep Learning training
- Deep Learning inference
- (No-deep) Machine Learning or Artificial Intelligence
- Big Data processing, High Performance Data Analytics
- Physics Simulation
- Life Science Simulation
- Computational Science Simulation
- Other
- Not completed or Not displayed



Using an external software

Other

Not completed or Not displayed

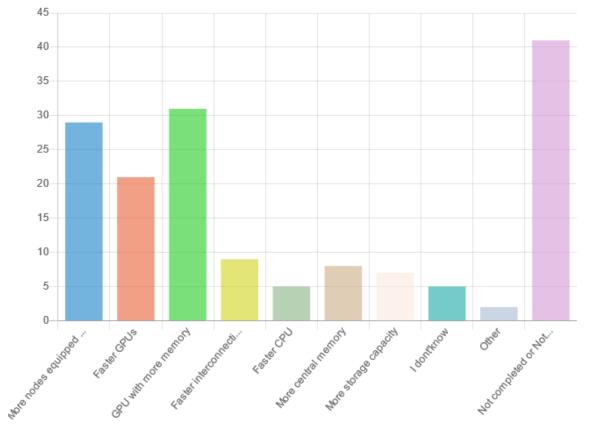


At which extend those claim are true ? 5 means "strongly agree" and 1 "strongly disagree".

[Faster and GPU with more memory would improve my productivity]

Arithmetic mean 4.08 Standard deviation 1.43 0-No answer

In what ways do you believe the HPC hardware could be enhanced to better suit your specific tasks or requirements?



Optimizing Deep Learning computing



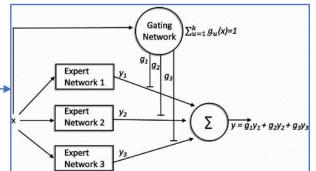
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- Tuning the ML project more efficienctly

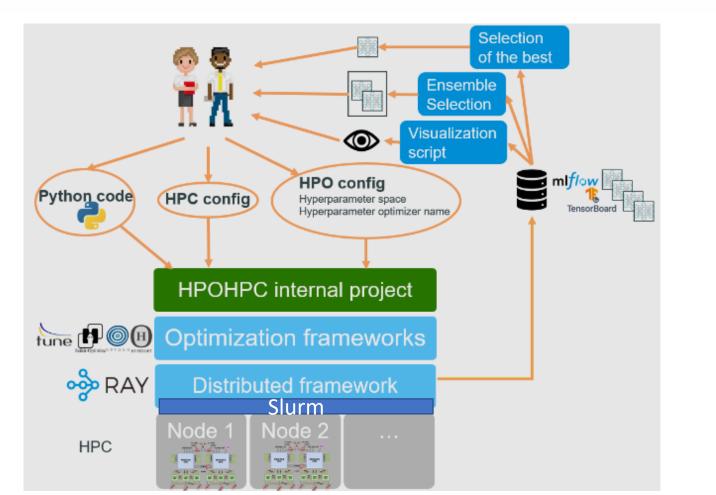
 Explore/Exploit
 Early-Stopping

 Improving the training speed

 Solution: Data-parallel SGD
 - Problem: Scalability issue
 - Solution: Mixture of Experts
- 3. Not enough memory
 - 1. Automatic Model Parallelism (GPIPETorch)
 - 2. Compute nodes on GPU <=> DAG stored on CPU (LMS by IBM)
- 4. Improving the inference speed
 - 1. DAG compiler&compiler such as Tensor-RT, ONNX-RT
 - 2. High-asynchronousity server or IoT



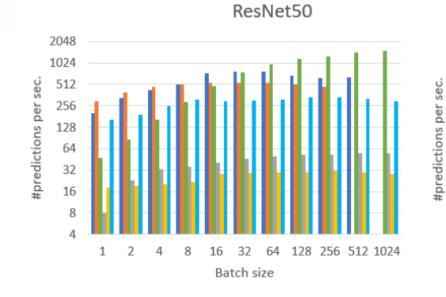
Example of system for optimizing users workload





Inference speed





DenseNet201

4096

2048

1024

512

256 128

64

32

16

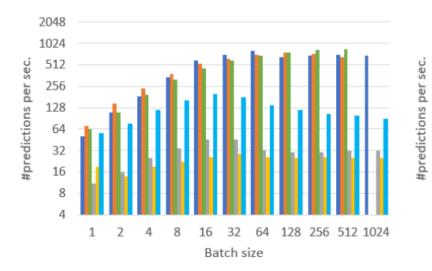
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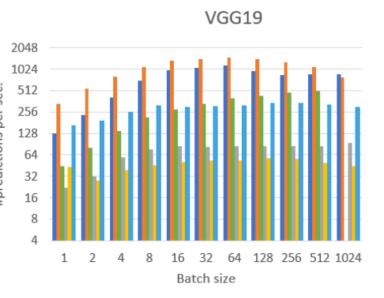
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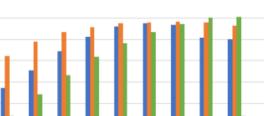
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EfficientNet-B0



64

32

Batch size

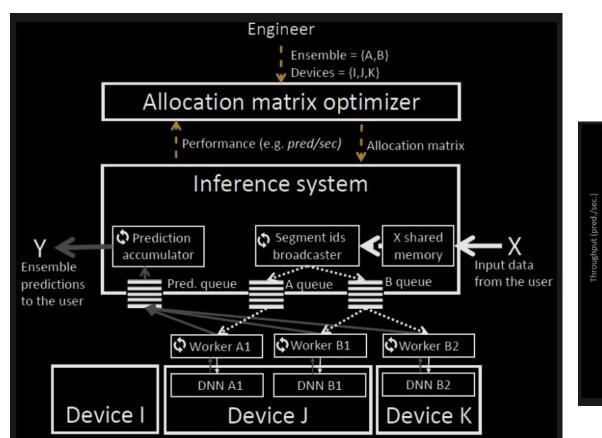
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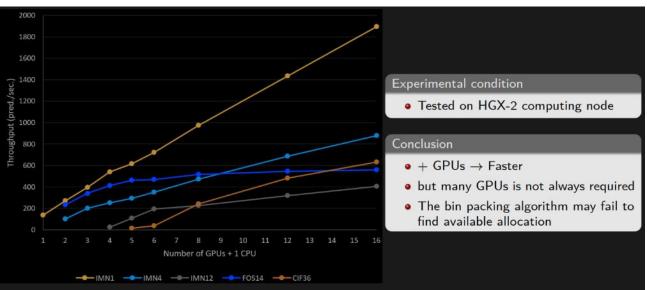
128 256 512 1024

Tensorflow GPU ONNX-RT GPU TensorRT GPU ■ Tensorflow CPU ONNX-RT CPU OpenVINO CPU

Legend

Inference server for Ensemble of Deep Neural Network





Weak Scalability Analysis



Many comments on the lack of GPU memory and GPUs:

- Not enough GPUs, not enough memory. I am thus using Colab or Lambdacloud instead.
- I experienced a time when the research was limited by the memory limitation of the GPU, even though I used the Tesla v100 32GB. Especially when I want to train a large graph-based deep learning model, I failed to train the network on big-size data.
- I cannot train/finetune very large language model due to memory constraint (A100/H100 x4 80Go required)
- Sometimes, I have to wait quite a long for my submitted jobs to be allocated

3. Accelerator trends



Other hardware

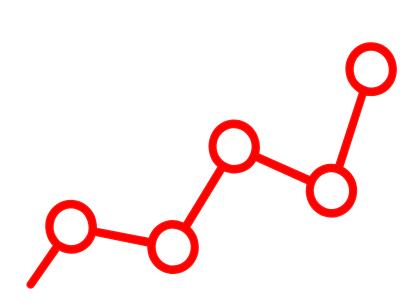


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Other hardware includes:

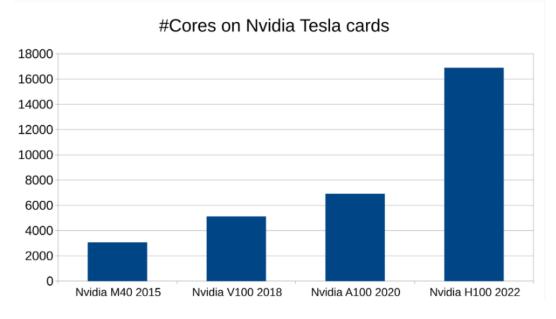
- Quantum computing
- Opto-electronic computing
- Neuromorphic for Spiking Neural Network (E.g., SpiNNaker)



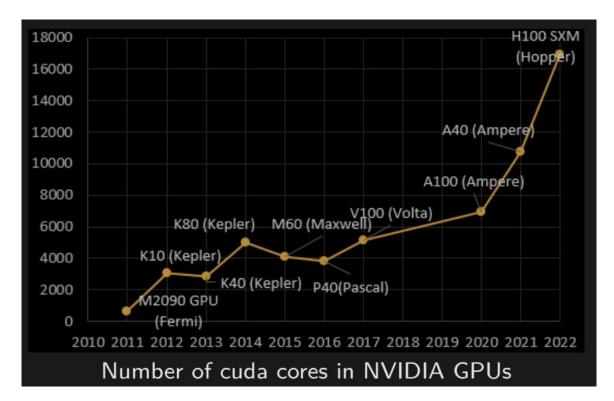
3. Accelerator trends



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Retrieved from *P. Talbot. Introduction to CUDA Programming 2023. UL HPC School 2023.*

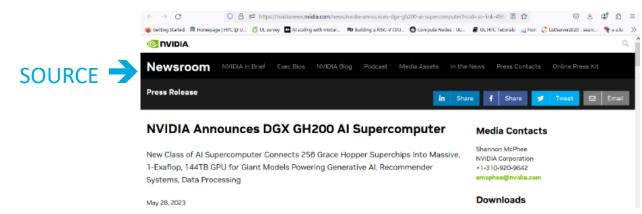


Retrieved from P. Pochelu. PhD Defense 2022 at Paris.



Challenge:

- The #cores will probably finish by plateau
- GPU-CPU connection may bottleneck some applications (e.g. short kernels)
- Scalability inter-GPU is still a challenge (Nvlink/Infiniband still not fast enough) Solution:
- This increases the bandwidth between GPU and CPU by 7x compared with the latest PCIe technology >>>
- 48x more NVLink bandwidth than the previous generation»
- « <u>NVIDIA Quantum-2 InfiniBand</u> networking to supercharge data throughput for training large AI models »



Second trend: Neuromorphic computing

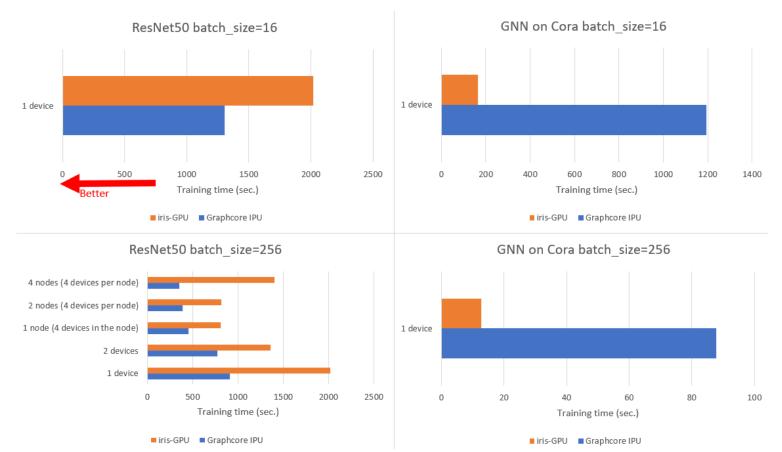


- From GPU code to IPU code:
 - From mono-GPU Tensorflow to mono-IPU (low code impact but version lag)
 - mono-GPU PyTorch
 mono-IPU PopTorch (stronger impact : <u>https://docs.graphcore.ai/projects/poptorch-user-guide/en/latest/pytorch_to_poptorch.html</u>)
 - Multi-GPU Tensorflow → Multi-IPU Tensorflow
- Strong performance difference according the model (computing graph):
 - URL: <u>https://docs.graphcore.ai/projects/memory-performance-optimisation/en/latest/optimising-performance.html</u>

Good for	Training large models
	Training with large global batch sizes
Not recommended	Low-latency inference

■ Compilation from a few seconds (GPU) to a few minutes (IPU) → Caching mechanisms

Standard Deep Learning operation + CPU RNG produce similar results (diff.<1E-6) Preliminary results: IPU can be from x2 faster to x7 slower according the DAG



Official code for testing:

- tf.keras.applications.ResNet50 (20M param.)
- <u>https://keras.io/examples/graph/gnn_citations/</u> (60K param.)

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LUXEMBOURG

Preliminary conclusion:

- IPU may scale better
- IPU may be significantly faster or slower according the application
- Any heuristics to know if a DAG will be faster on IPL than GPU ? (nodes, parameters, ...)

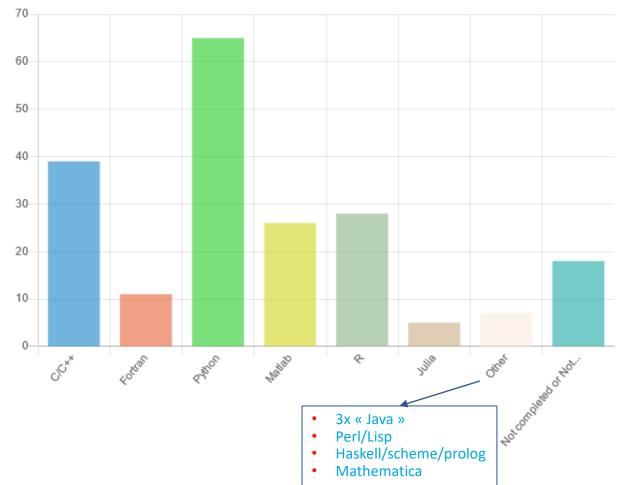
4. User learning activity (User perspective)



Skills users have



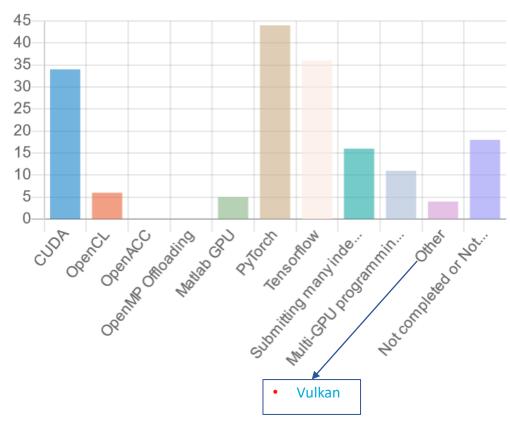
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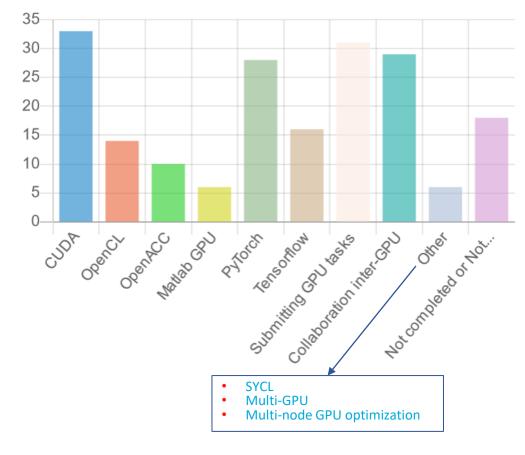
Which programming language have you already used ?



Which GPU-related skills have you already demonstrated?



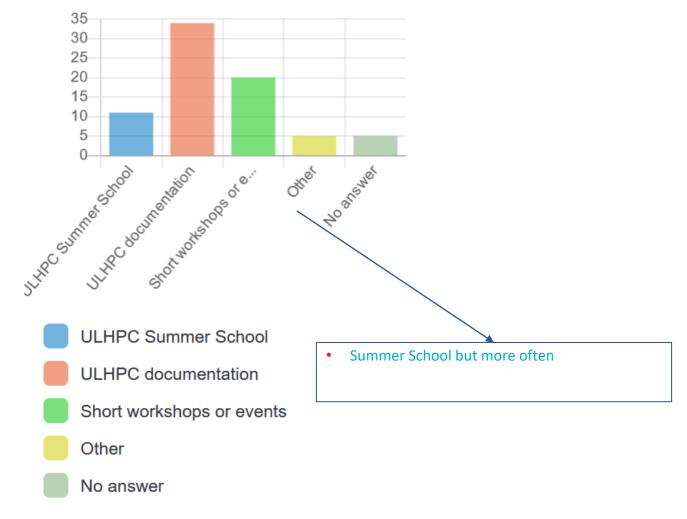
What would you like to learn more ?



GPU environment



What learning option do you find most appealing when it comes to exploring new technologies?



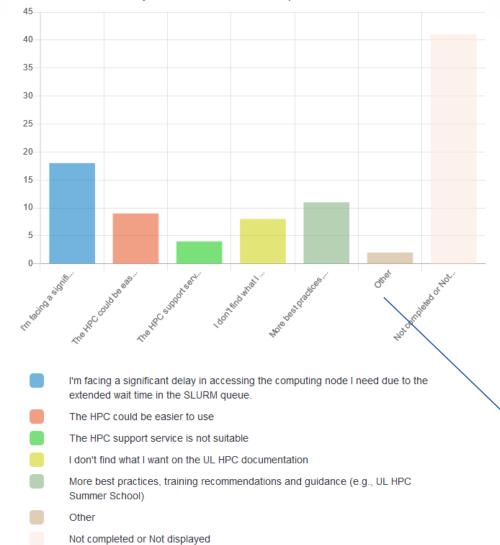
5. Conclusion & My actions



Global question







The support of the HPC staff is really good. For the future it would be interesting to hire IT experts to do programming work on a limited time basis for projects.

Outline

Conclusion:



What I can do (or doing)

- GPU are today fundamental for HPC users
 - Feels more GPUs is required
 - Feels more memory is required
 - Feels faster GPU would be great
- The future of accelerators seems:
 - Faster interconnections
 - Diversity in accelerators (IPU, FPGA, ...)
- Eager to learn GPUs
 - Find difficulties (eg memory issue) but does not open tickets

- Saving with workload optimization (such as Hyperband)
- For memory issue: Automatic Model Parallelism technics (GPipeTorch), GPU<->CPU callbacks
- → For speed issue: Data-Parallel SGD (Horovod)
- Both: Mixture-of-Experts instead of 1 big model. Better for scaling.
- → Tuto: Horovod, NCCL, GPU RDMA, ...
- IPU benchmark and doing recommandations
- Opening slack channel dedicated to GPU for research support (HLST team)
- Monitoring the userbase (sometimes what is need is not explicit, collecting data for research)

Thank you for your attention

Hungry ? 🙂



Appendix



- TITLE: Optimizing GPU Usage for Deep Learning Workloads: Insights and Strategies for Enhanced Efficiency
- ABSTRACT:
- Demand for GPUs in super computing has recently shown tremendous growth as the technology is spearheading advancements in AI, drug research, medical imaging, financial modeling and numerous other domains. It is therefore crucial to understand and optimize their usage for deep learning workloads.
- In response to this computational need, this talk focuses on key concepts related to GPU usage. First, we present the various types of deep learning workloads and the challenges they pose in terms of performance and programming. Second, we delve into state of the art parallelization and optimization techniques that can effectively enhance GPU usage. At last, we explore the latest hardware technologies that have the potential to significantly improve the efficiency of AI workloads.
- The insights shared in this talk are based on past experiences, recent experiments, a survey conducted among over 75 UL HPC users, and valuable information provided by the PCOG team. All of these elements will be showcased during the presentation.